516-57 DRW 1/11/72

FORMAT FOR THE 516-NODE-T.I.U. SPIDER INTERFACE

AL MARKS

A. Write Command to T.I.U.

MS_16	6		9	8	7	6	5	4	1	LS
sc	cø	•••	SC7	write SC	Spare	Chan taken	Reset TIU	Node Mask	Reg	

B. Write Data to T.I.U.

MS <u>16</u>		9	8	7	•	· 1	\mathbf{LS}
WDØ	•••	WD7	W Sig	X		X	

C. Read Data from T.I.U.

MS_16		9	8	7	6	5				· 1	LS
RDØ	•••	Rd 7	R Sig	Rd In	TRB	Spare	Х	X	х	X	

D. Read Status from T.I.U.

MS	16		11	10 7	6	5	4	3	2	1	LS
	RSC2	• • •	RSC7	Node Status	Pwr on	W Ch Ready	Rd Ch In	TRB	RSCØ	RSC1	
SC = Select Channel R. Sig = Read Signal											

WD = Write DataRd In = Read Data InRD = Read DataTRB = T.I.U. Trouble FlagWSelCh = Write Sel ChannelSP = SpareWSig = Write SignalRC = Read Channel

Write Data Ready will cause a Write Interrupt. Read Data In will cause a Read Interrupt.
Read Channel In will cause an Awake Interrupt.
Read Channel In or a Trouble Flag will cause a Status Interrupt.
Write Command bit 8 = 1 will cause the writing of Select Channel
 into the T.I.U.

516-57 - <u>REVISION</u> DRW 1/25/72

FORMAT FOR THE 516-NODE-T.I.U. SPIDER INTERFACE

A. Write Command to T.I.U.

MS	1		8	. 9	10	11	12	13	16	LS
	scø	•••	SC7	write SC	Spare	Chan taken	Reset TIU	Node Mask	Reg	

B. Write Data to T.I.U.

MS_1			9	10	16 LS
WDØ	•••	WD7	W Sig	x	x

C. Read Data from T.I.U.

MS	1	·	8	9	10	11	12		•	•	16	LS
	RDØ	• • •	Rd 7	R Sig	Rd In	TRB	Spare	x	X	X	X	

D. Read Status from T.I.U. /

MS	1		<u>8 ·</u>	<u>· 9 10</u>	<u>.</u> 11	12	13	14	15	16	LS
	RSC2	• • •	RSC7	Node Status	Pwr on	W Ch Ready	Rd Ch In	TRB	RSCØ	RSC1	

SC = Select ChannelR. Sig = Read SignalWD = Write DataRd In = Read Data InRD = Read DataRd In = Read Data InWSelCh = Write Sel ChannelSP = SpareWSig = Write SignalRC = Read Channel

Write Data Ready will cause a Write Interrupt. Read Data In will cause a Read Interrupt. Read Channel In will cause an Awake Interrupt. Read Channel In or a Trouble Flag will cause a Status Interrupt. Write Command bit 8 = 1 will cause the writing of Select Channel into the T.I.U.