516 - 69 JVC 6/23/72

GLANCE "G"

COMMUNICATION FORMAT: TSS-516 TO SCOPE

• WRITE COMMANDS		INTERRUPTS			
MSB	(12 BITS)	S	R	W	Α
AR1	ADDRESS	0	1	0	X
AR2	ADDRESS	0	1	1	Х
ARl	ADDRESS	1	1	0	Х
AR2	ADDRESS	1	1	1_	X
000000000000000000000000000000000000000		X	0	Х	X.

WRITE M1 (FOLLOW WITH WD)
WRITE M2 (FOLLOW WITH WD)
READ M1 (FOLLOW WITH RD)
READ M2 (FOLLOW WITH RD)
START DISPLAY

M1 = LIST MEMORY

M2 = CHARACTER SET MEMORY

AR1 = ADDRESS REGISTER FOR MEMORY 1 AR2 = ADDRESS REGISTER FOR MEMORY 2

WD = WRITE DATA RD = READ DATA

Address registers are automatically incremented after each "WRITE DATA" or "READ DATA" command.

## **EXAMPLE:**

MEMORY	FUNCTION	FORMAT	
1	AT ADDRESS O, WRITE 100000	C4 W100000	
1	AT ADDRESS 1, WRITE 111111 AT ADDRESS 3, WRITE 133333	W11111 C64 W133333	
1	READ DATA FROM ADDRESSES 0 & 1	C14 R R	
1	READ DATA FROM ADDRESS 3	C74 R	
	START DISPLAY	C220	